

Figure 1

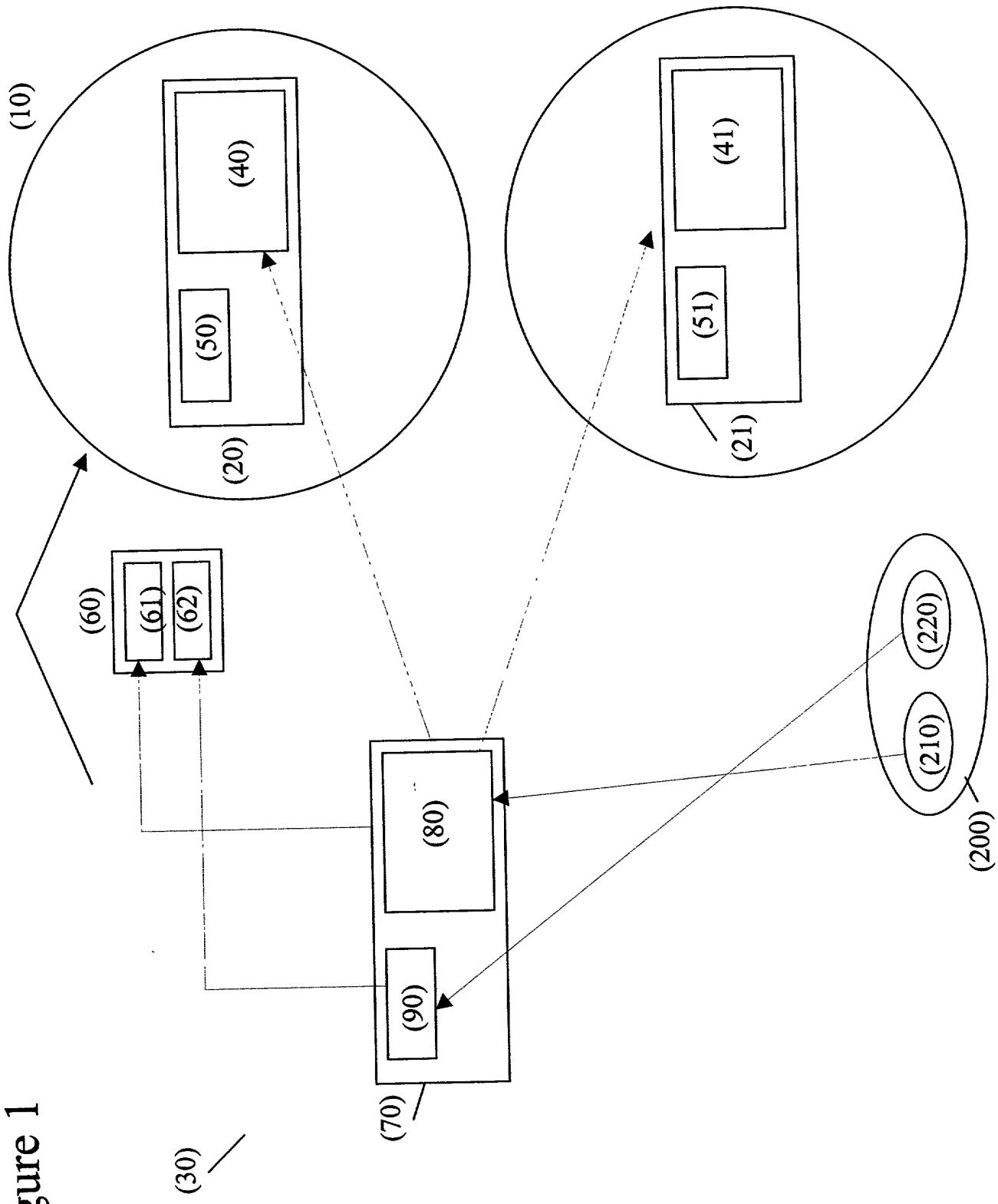
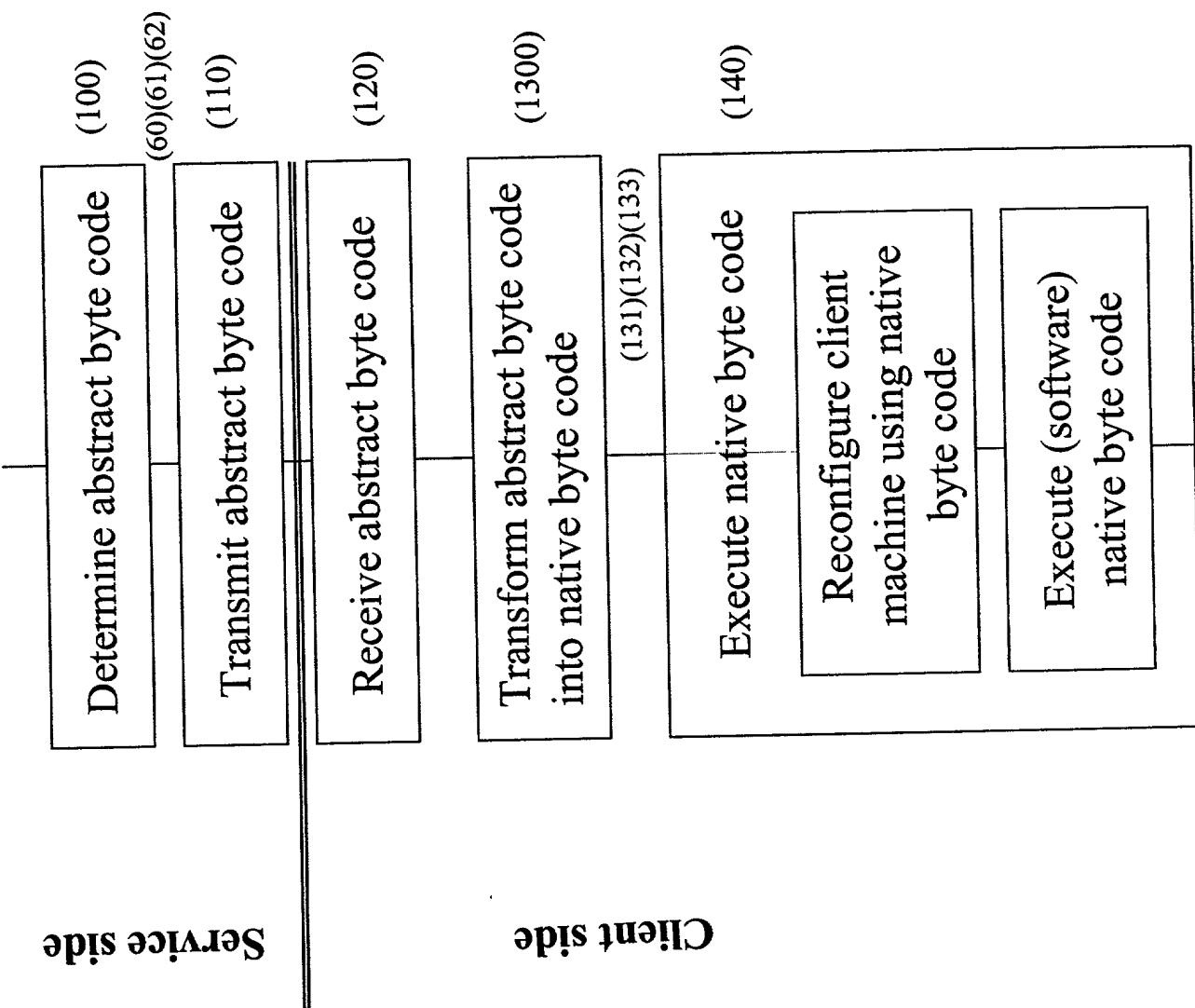


Figure 2



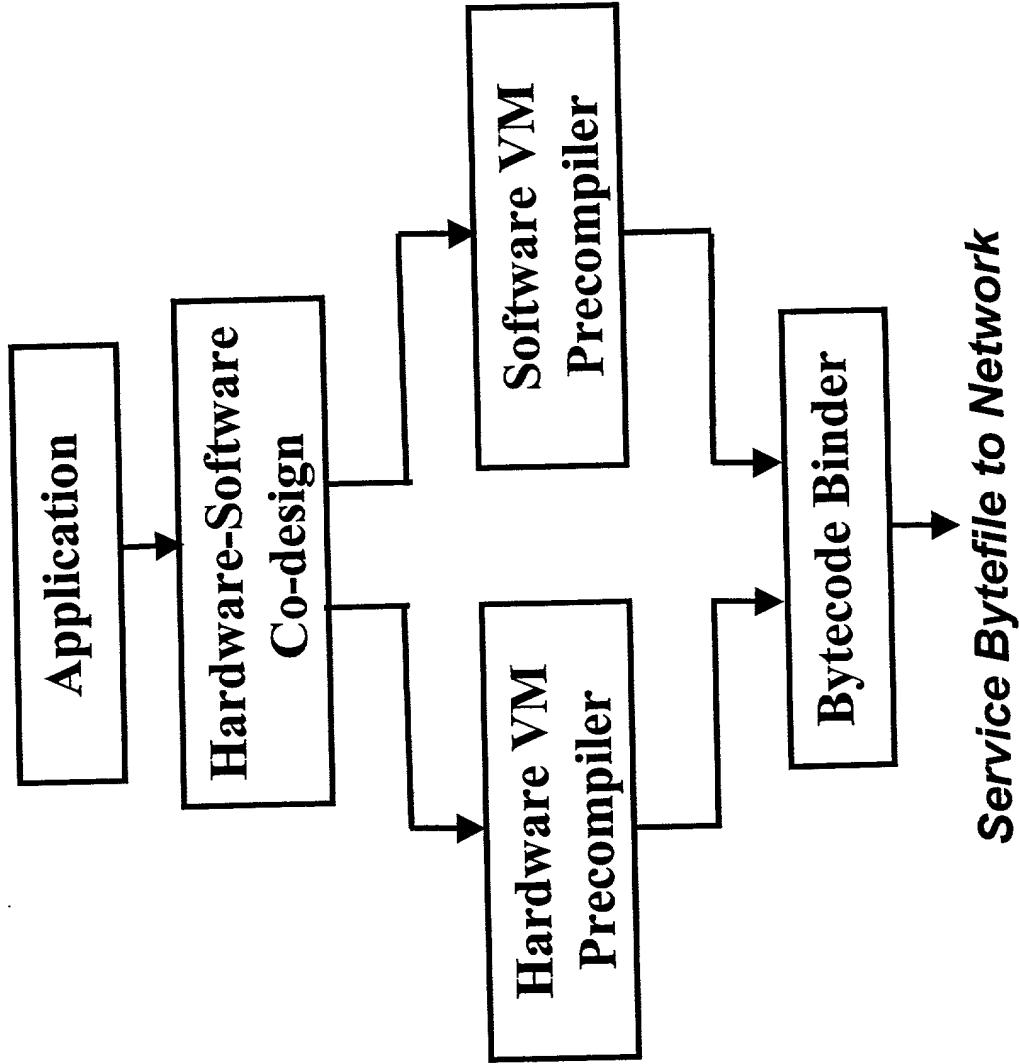


Figure 3

**Service Bytefile to Network**

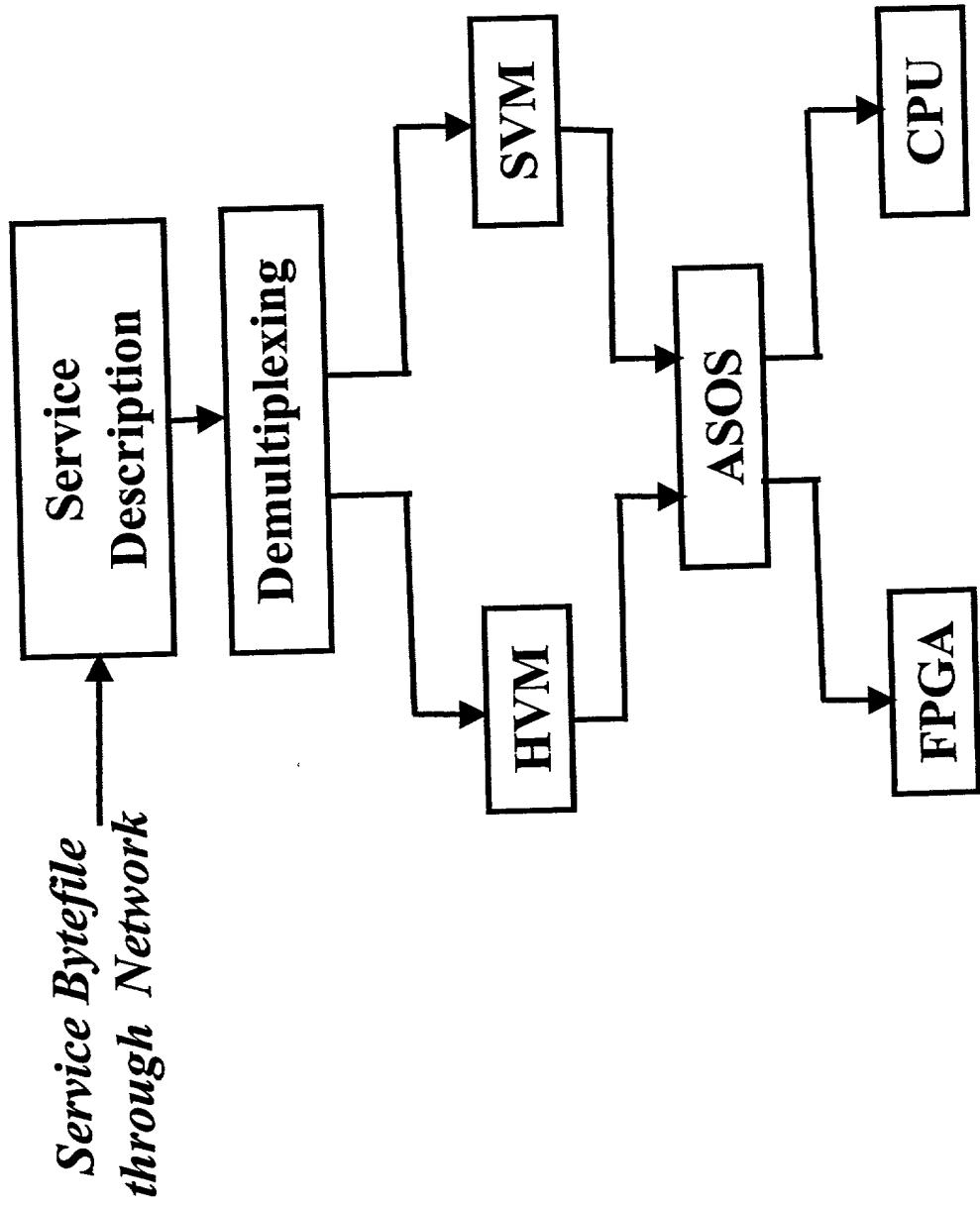


Figure 4

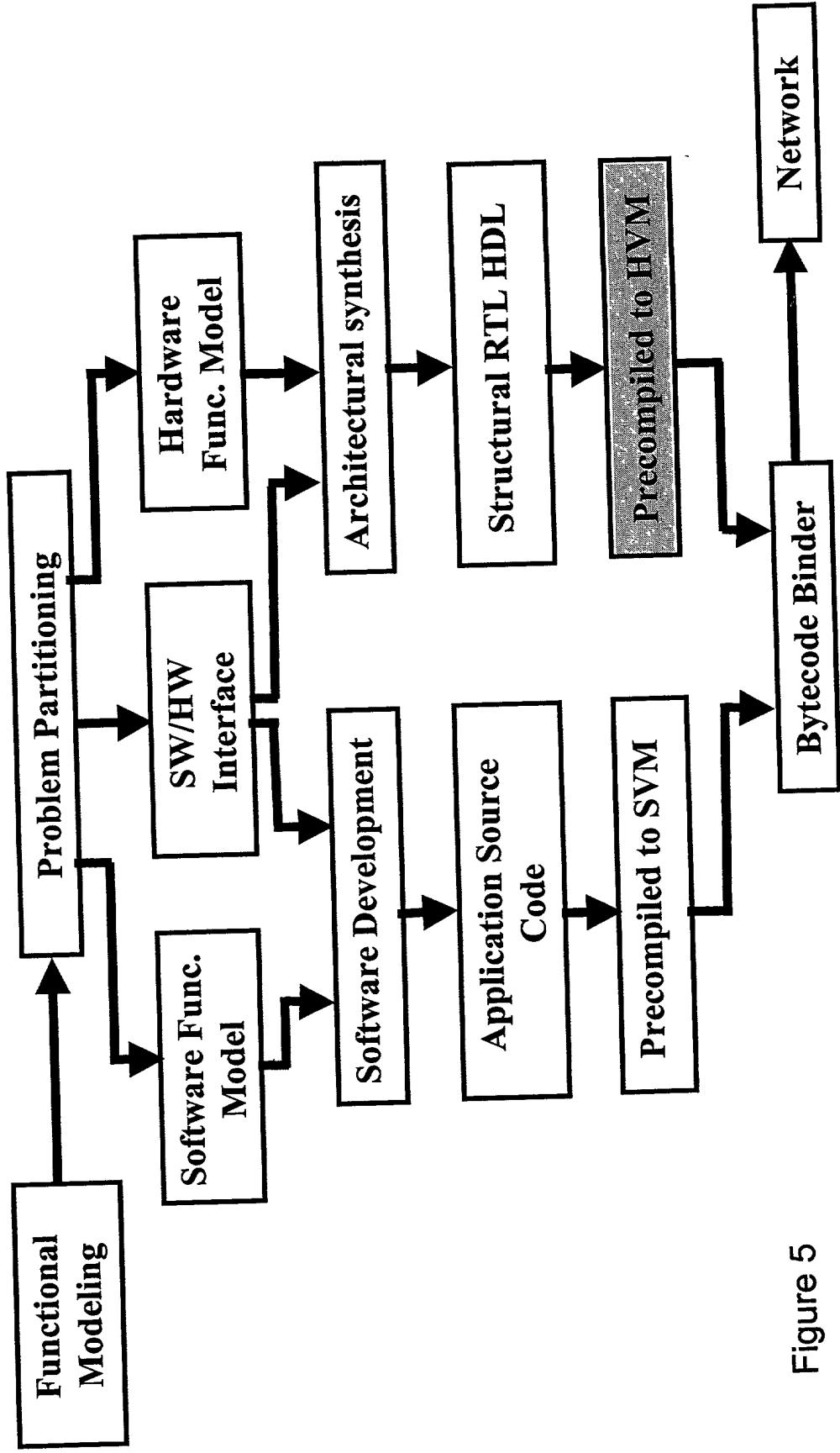


Figure 5

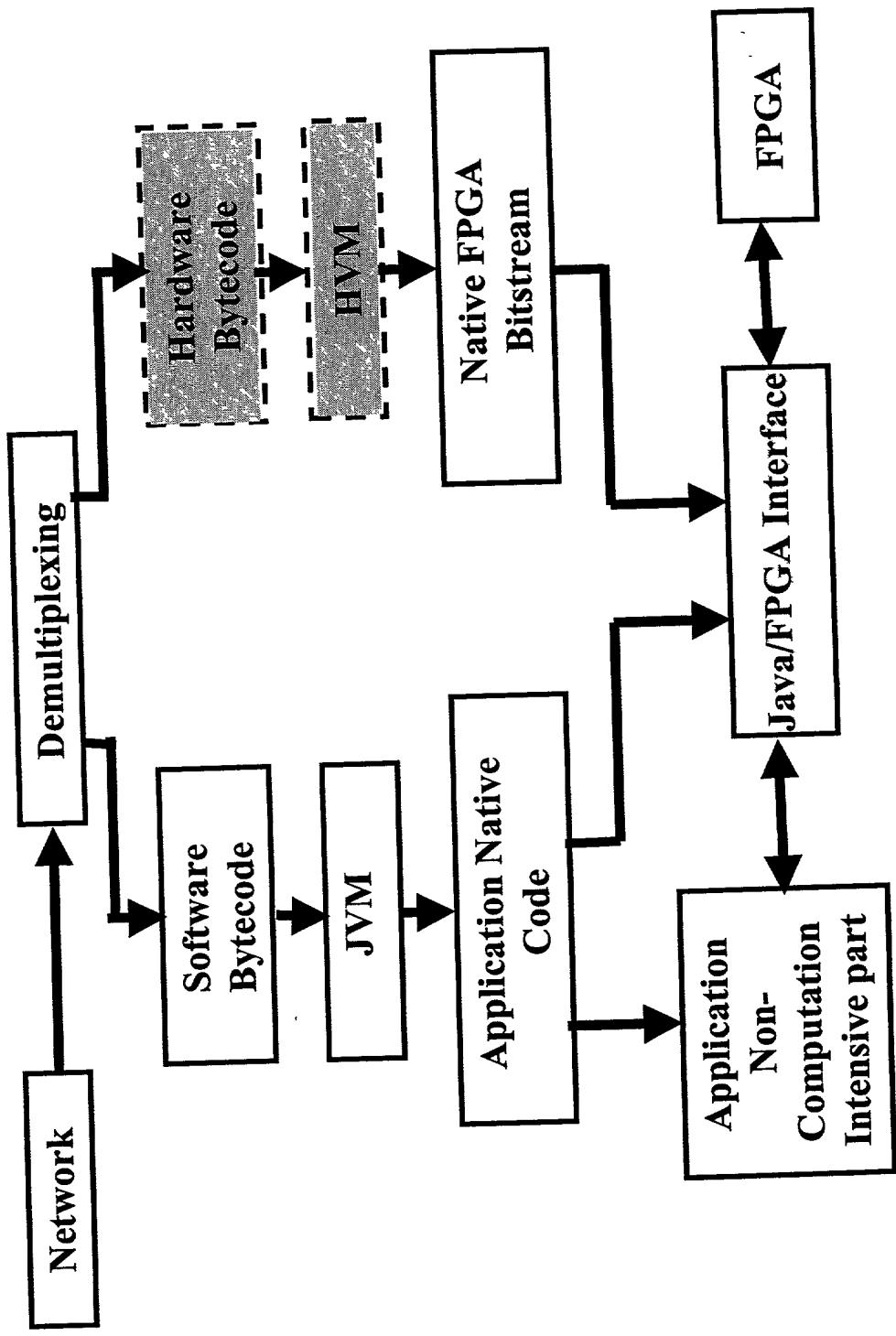


Figure 6

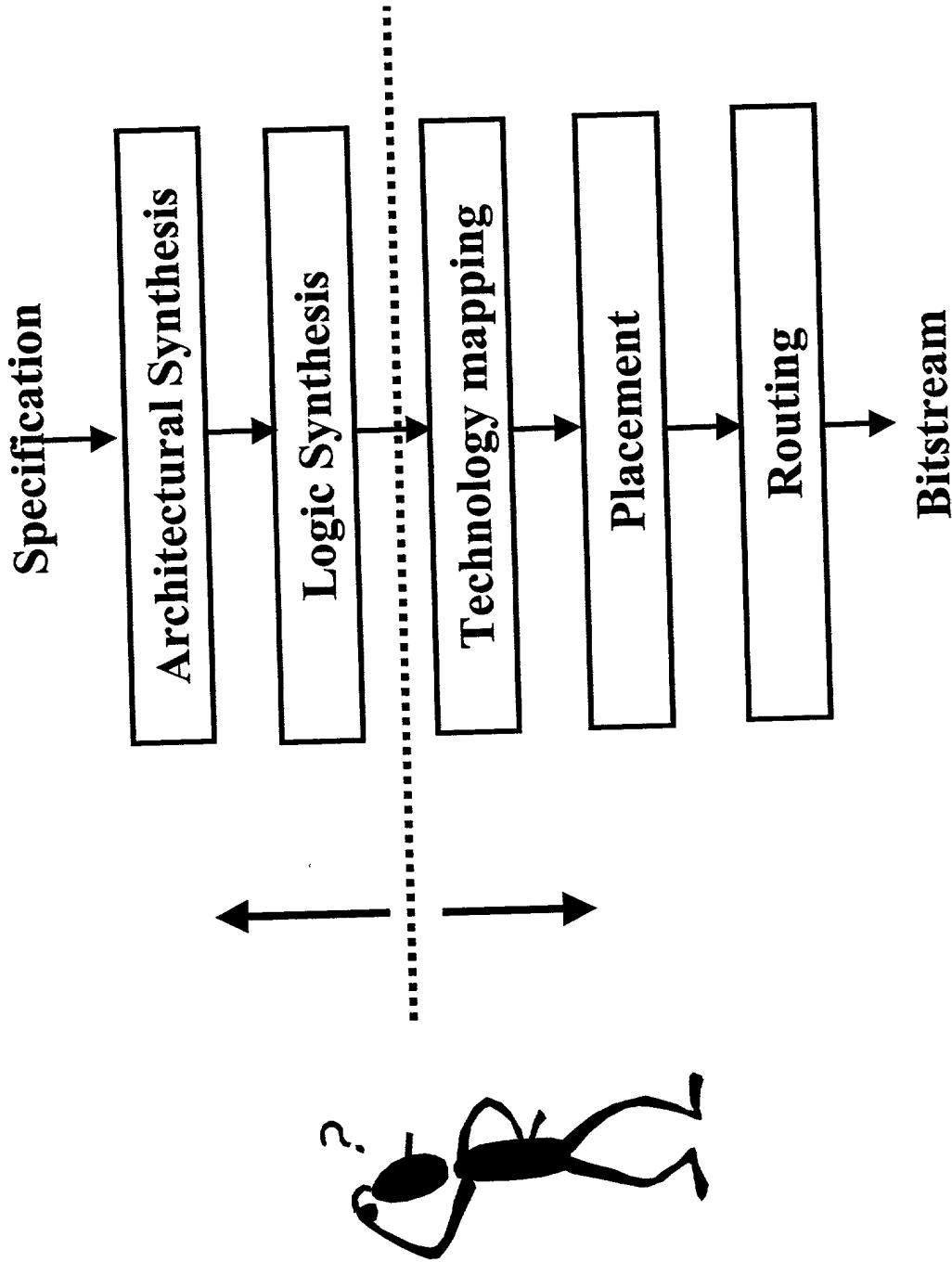


Figure 7

Figure 8

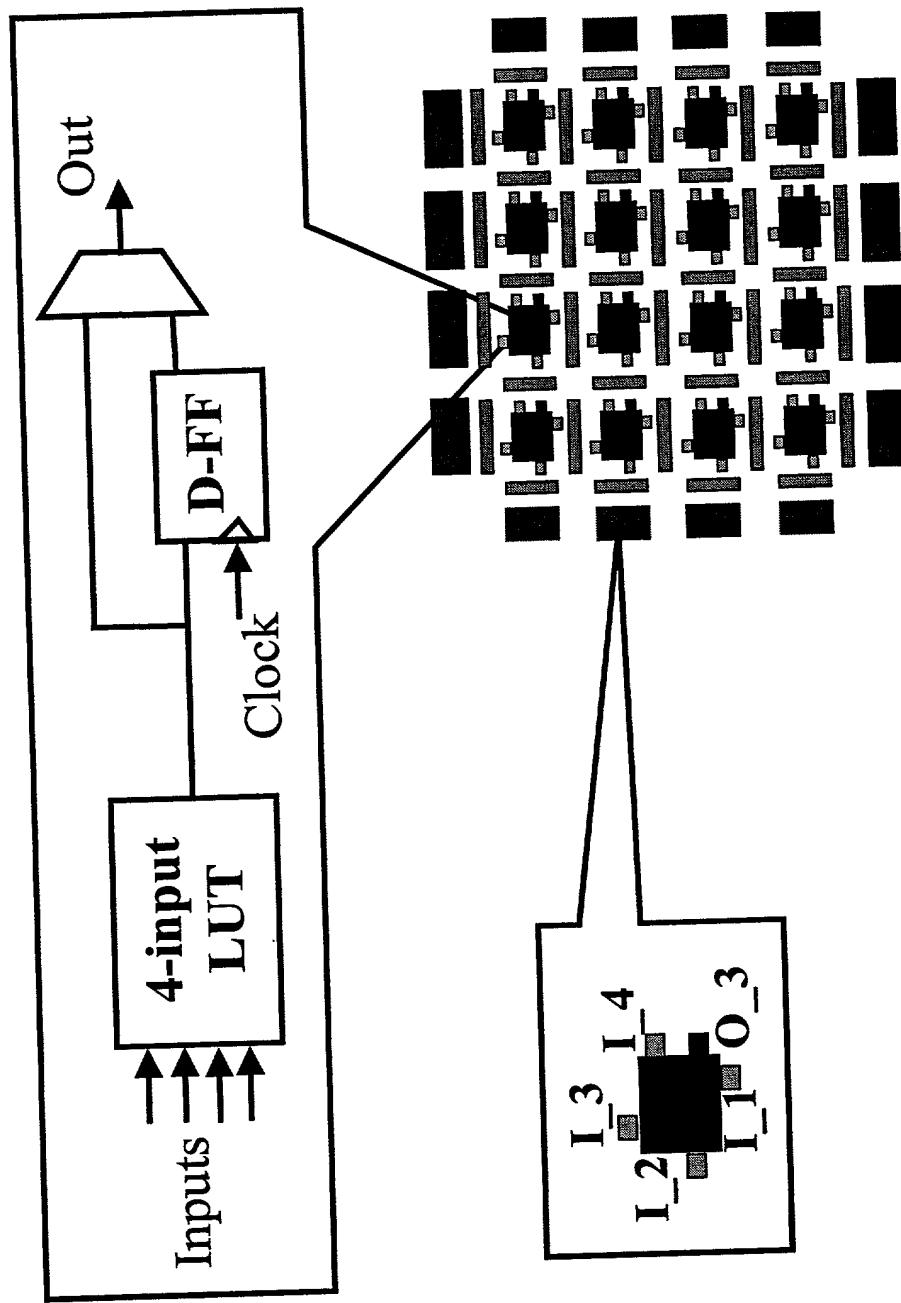
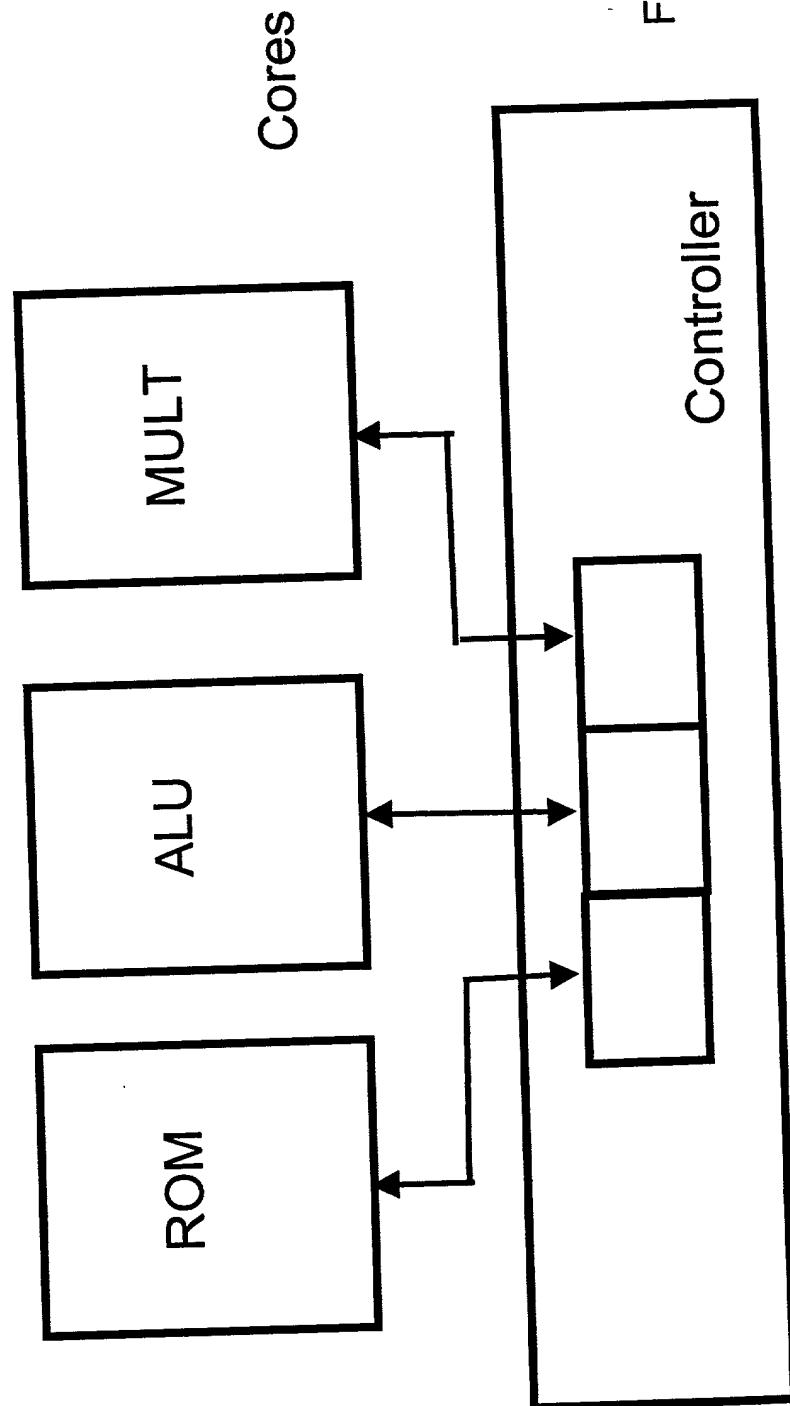
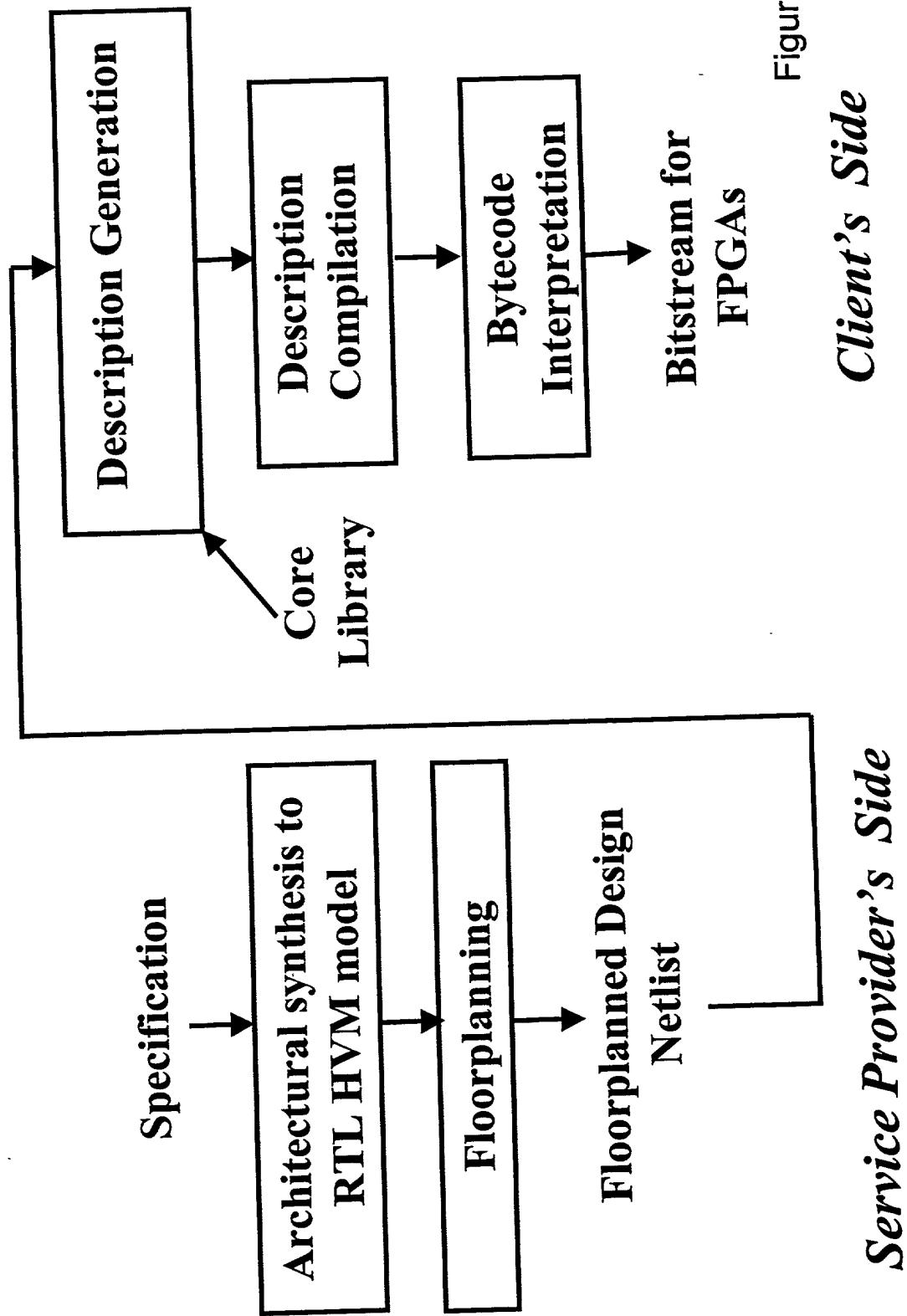


Figure 9





## *Service Provider's Side*

Client's Side

Figure 10

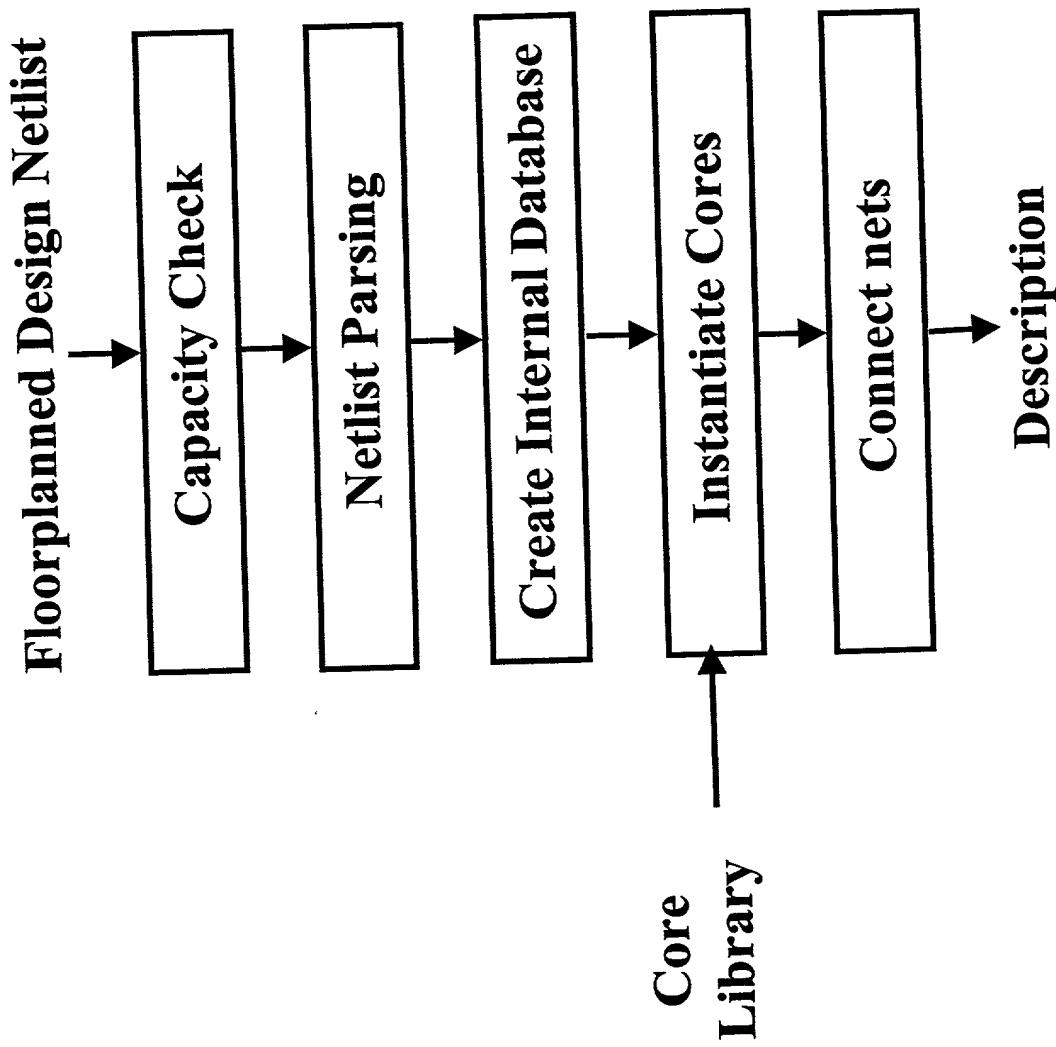
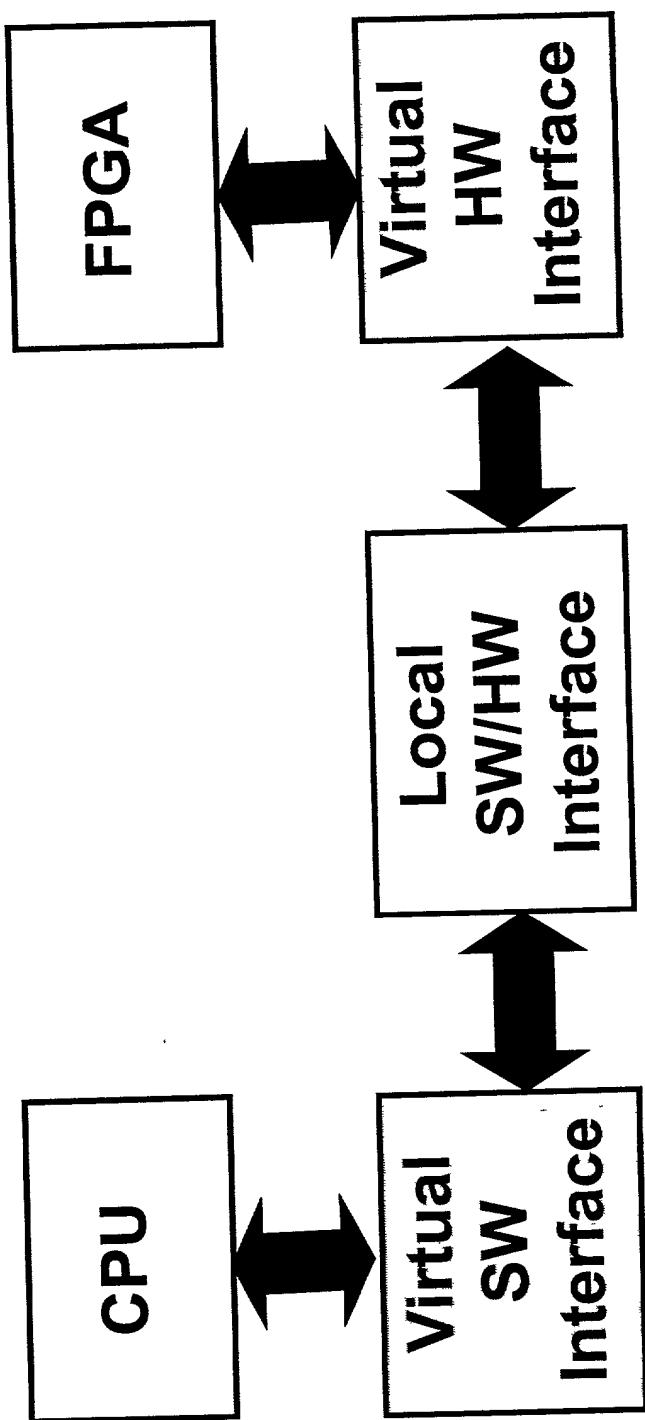


Figure 11

Figure 12



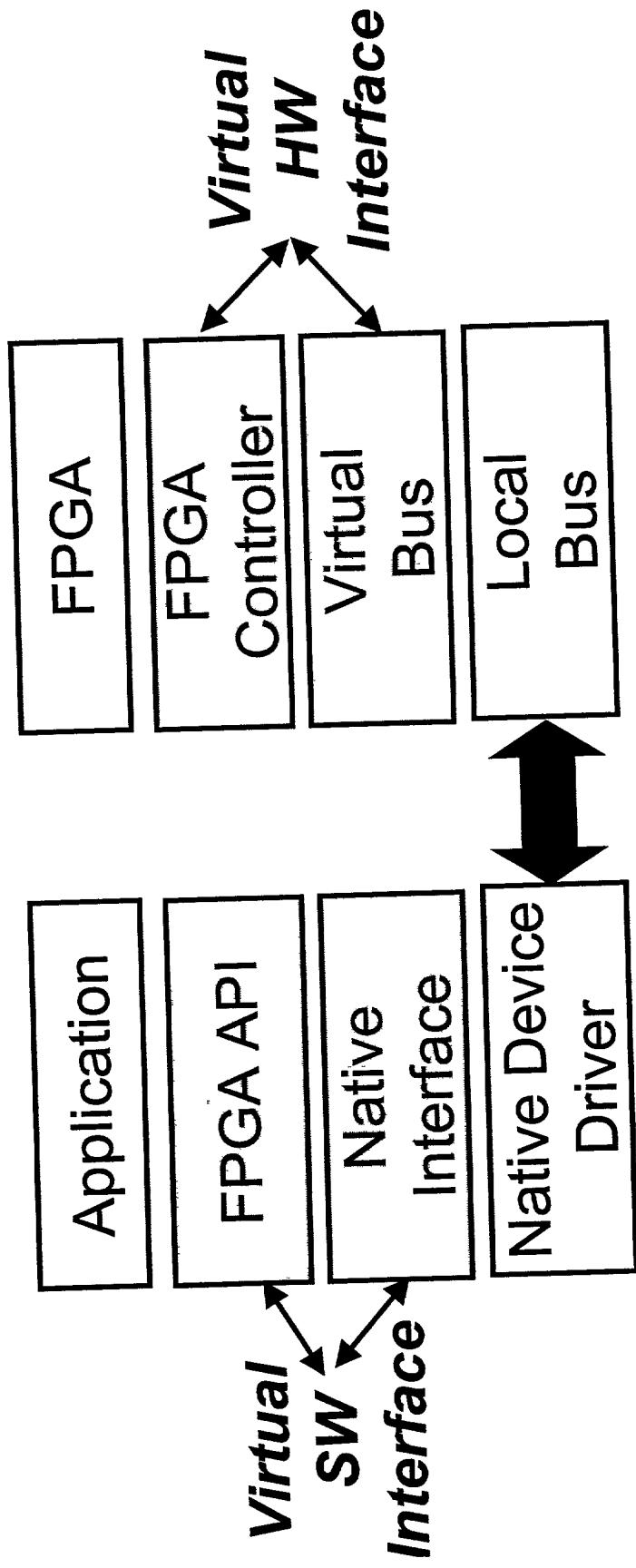


Figure 13

# An Abstract FPGA Model

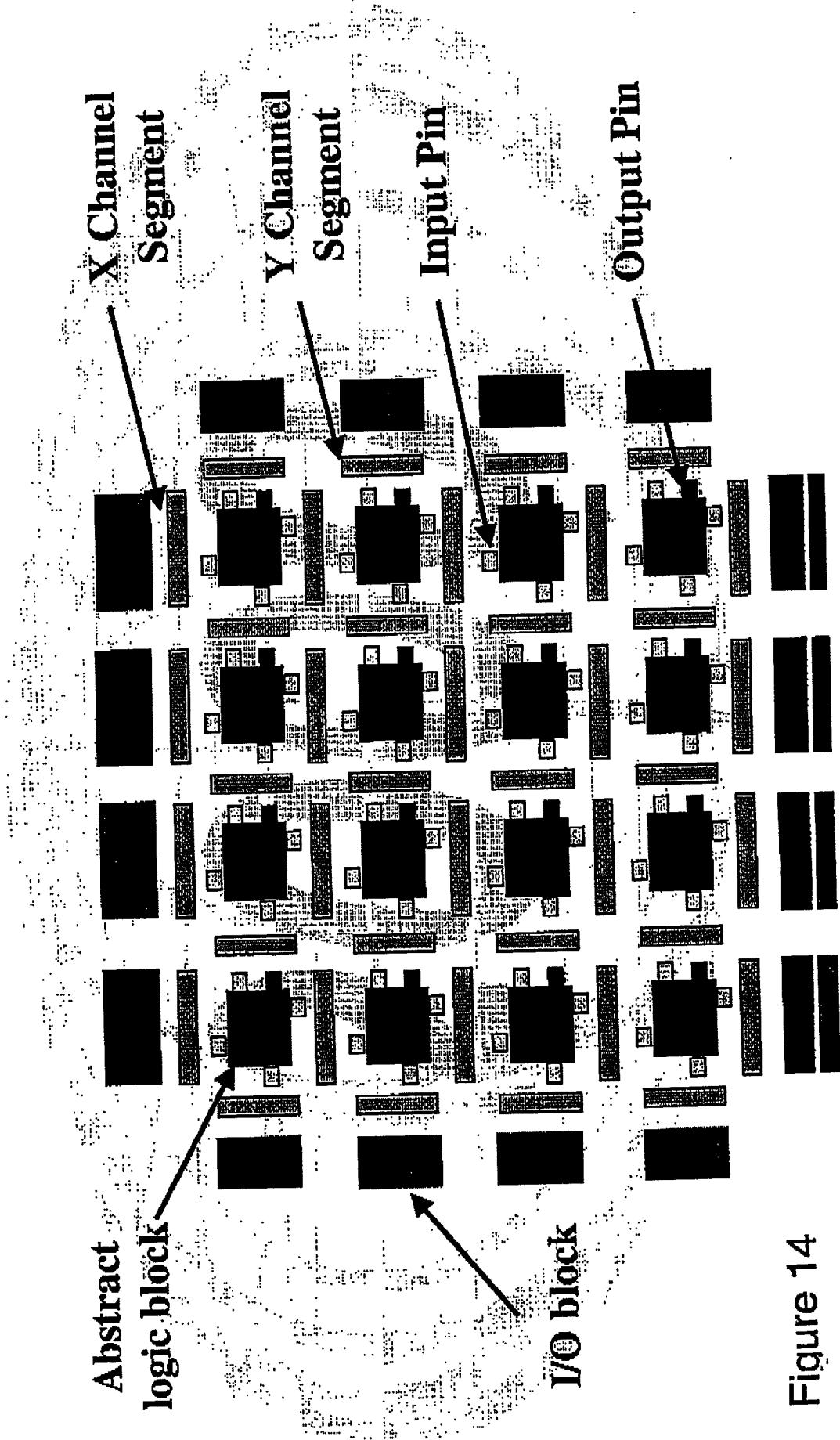


Figure 14